

FIG. 1  
(PRIOR ART)

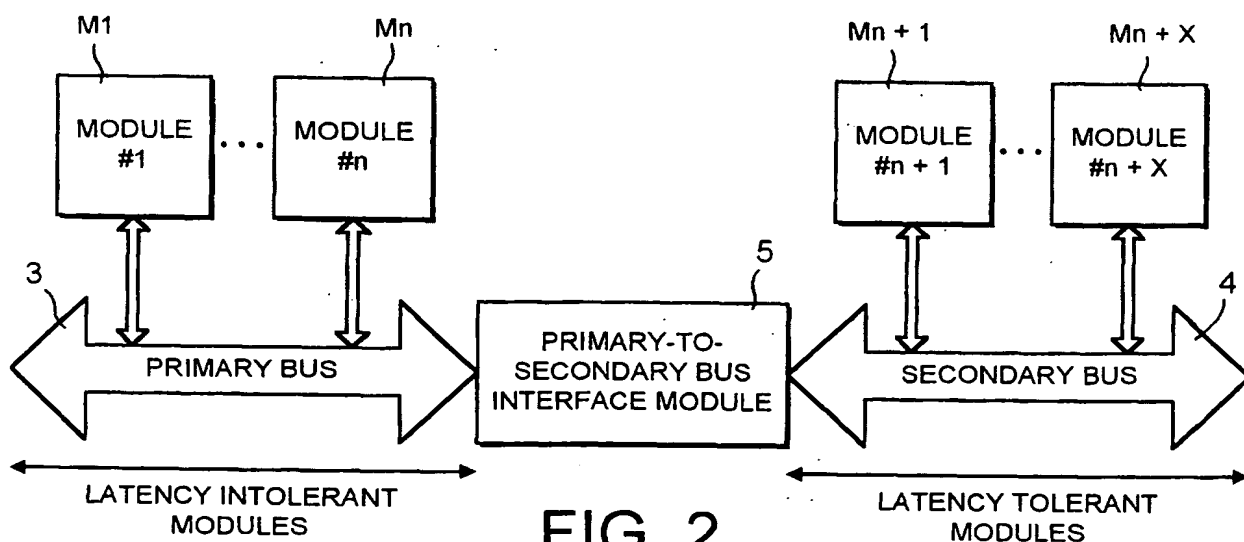


FIG. 2

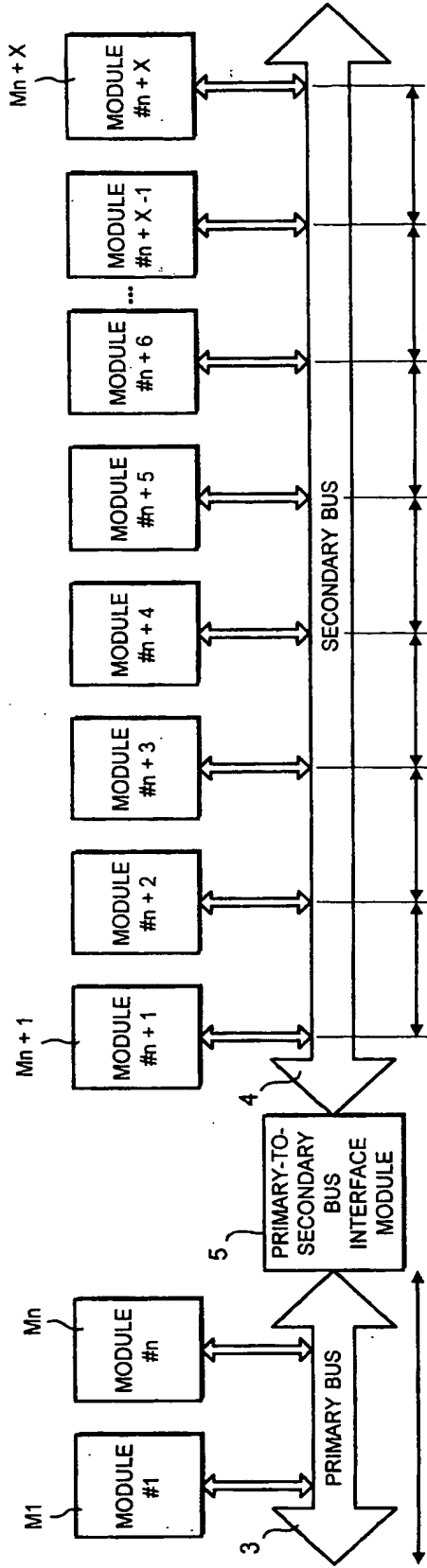


FIG. 3

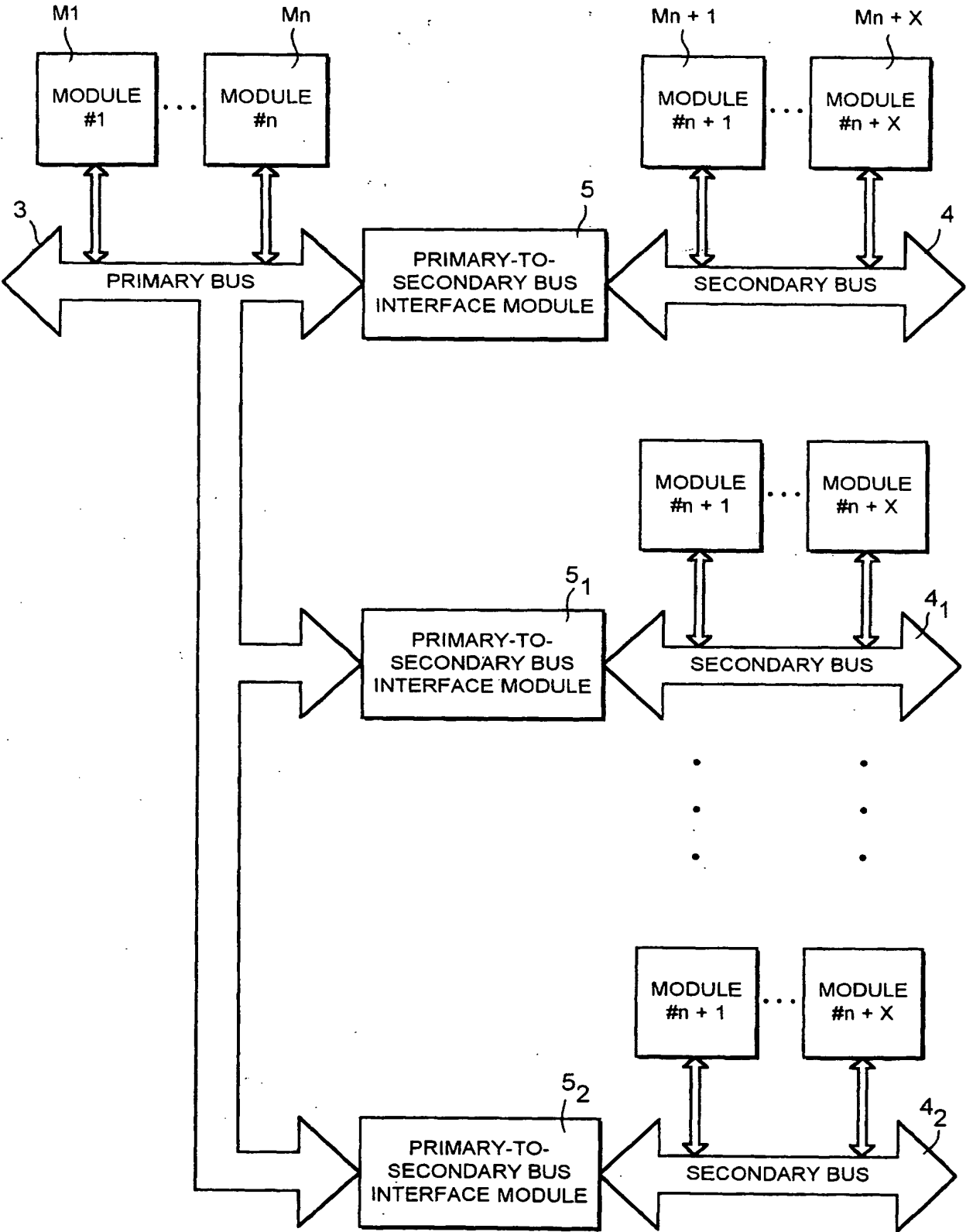


FIG. 4

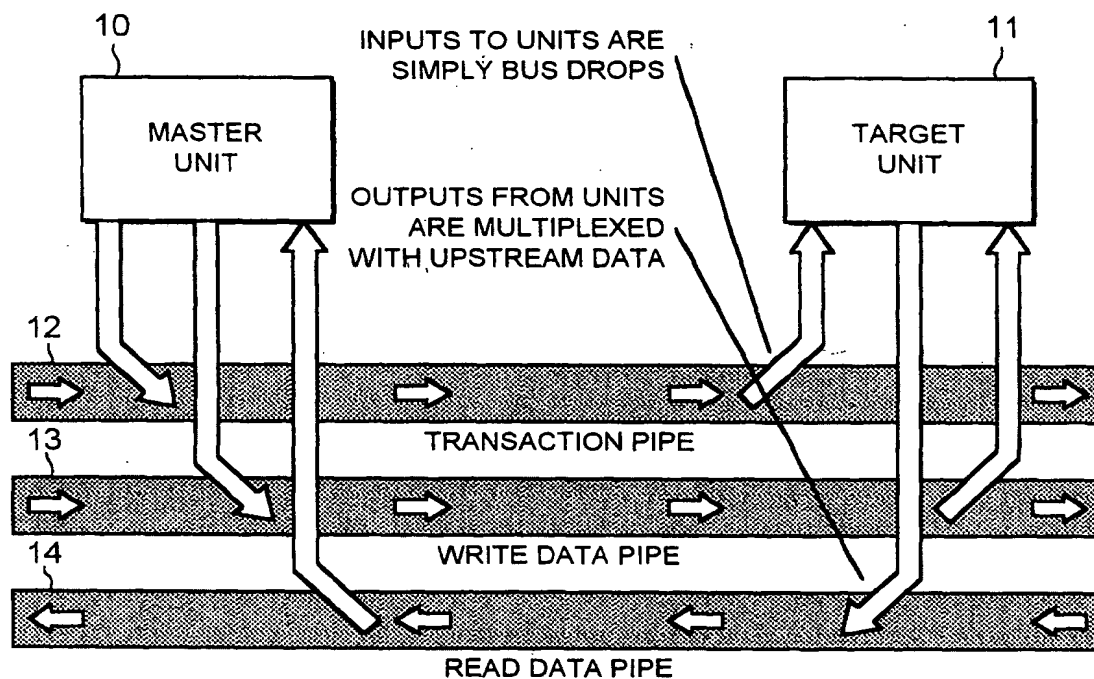


FIG. 5

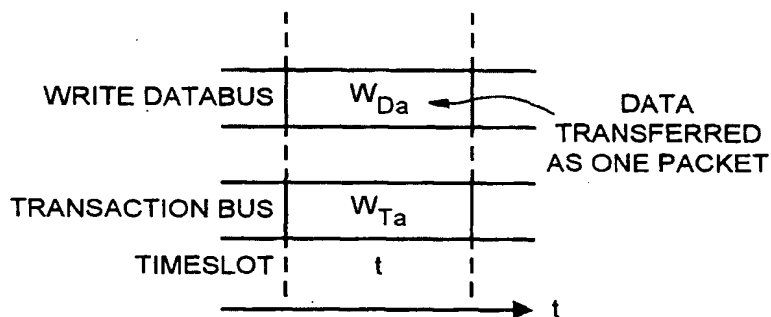


FIG. 6

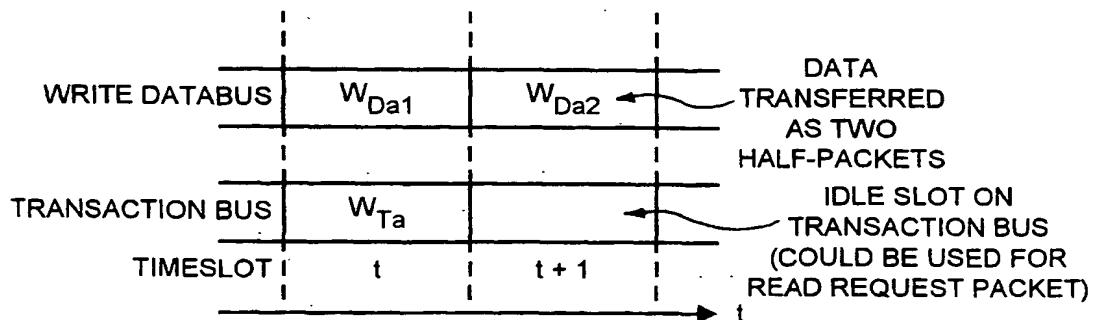


FIG. 7

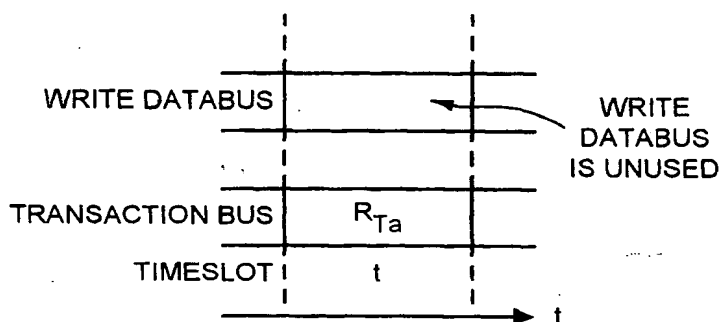


FIG. 8

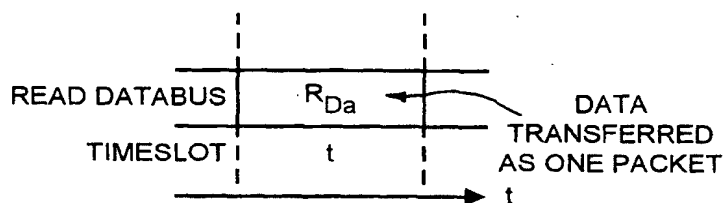


FIG. 9

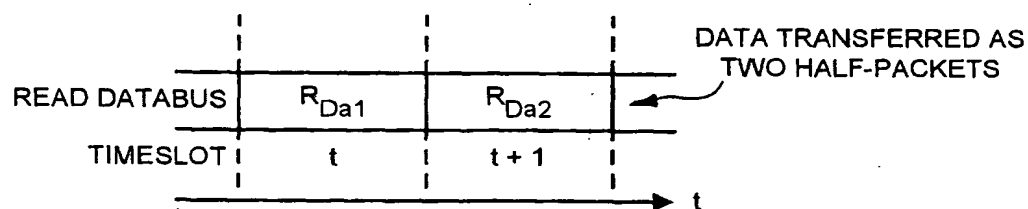
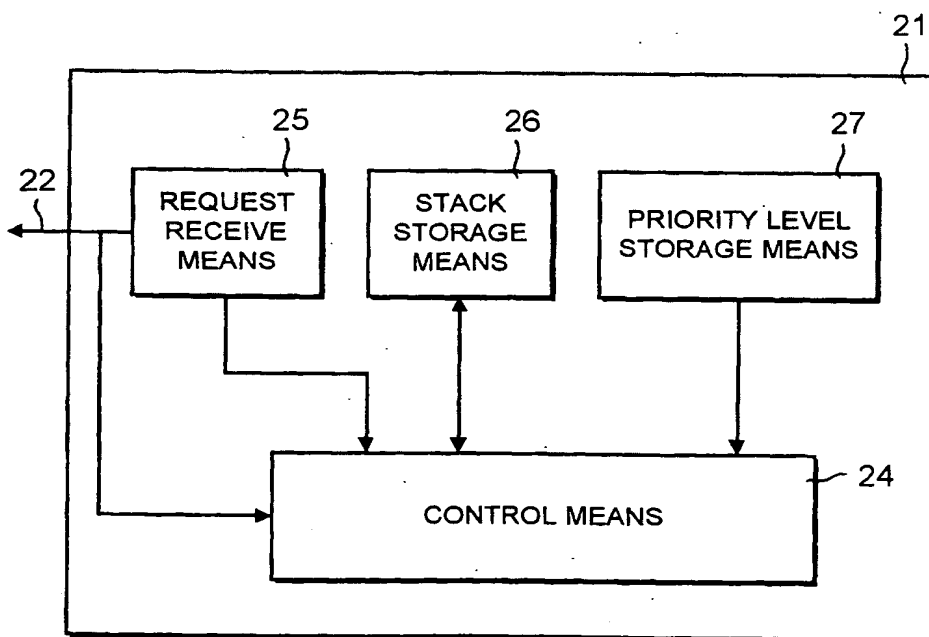
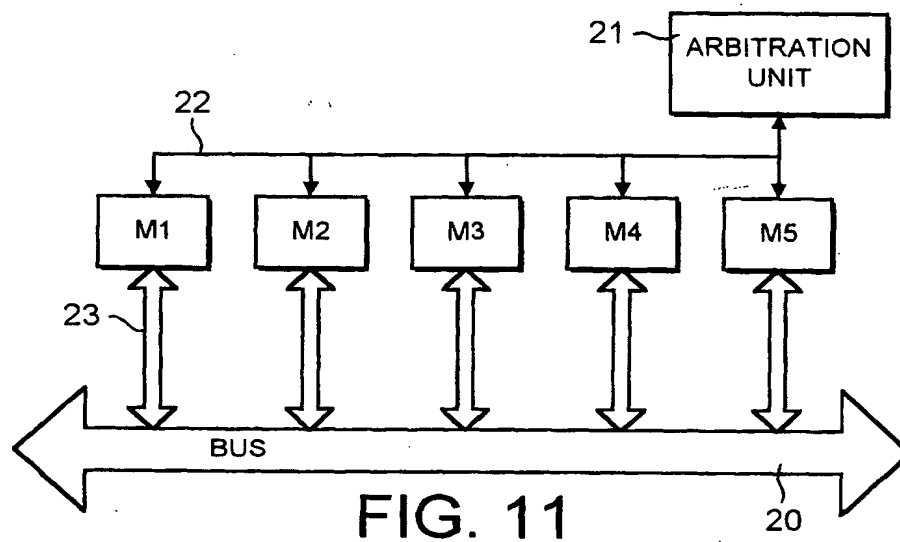


FIG. 10



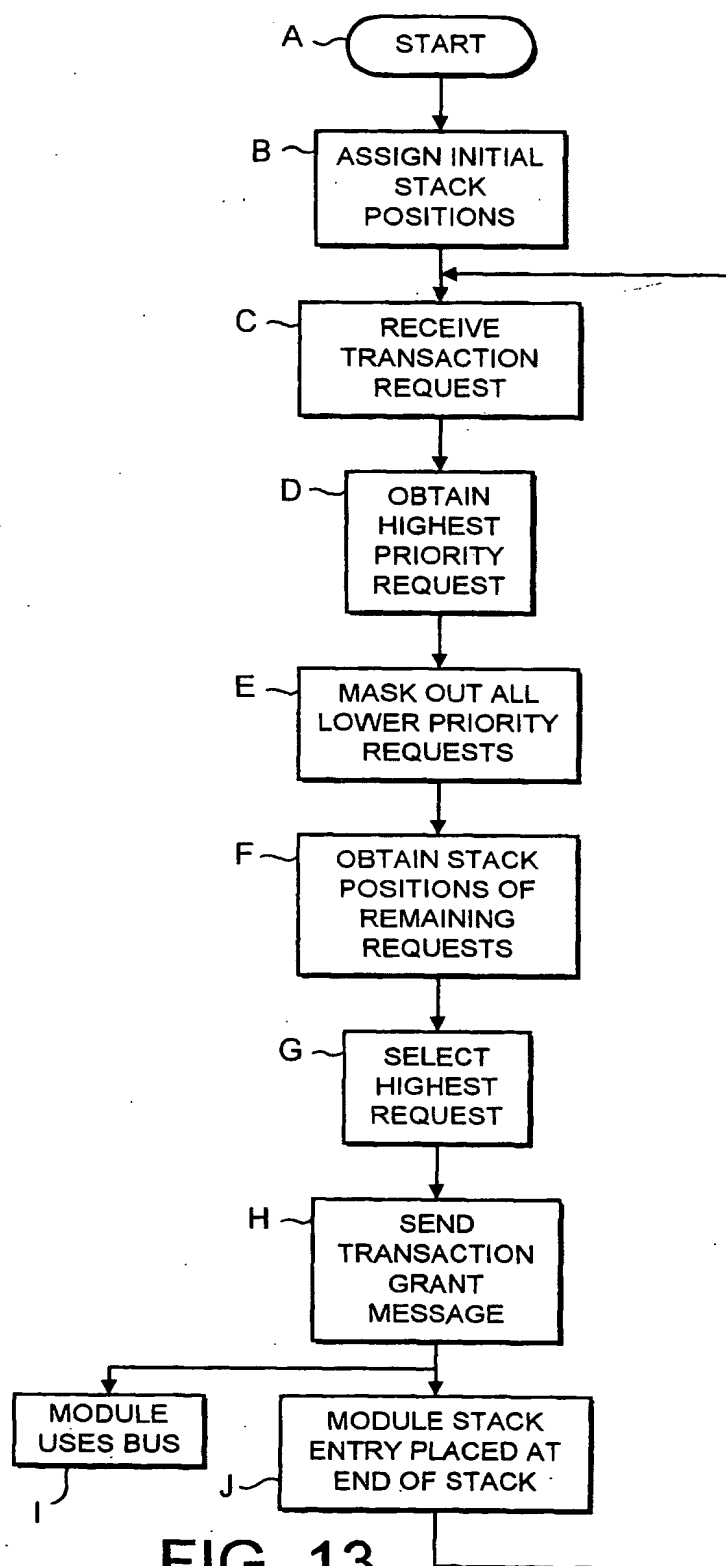


FIG. 13

28 MODULE	29 PRIORITY
M1	H
M2	M
M3	L
M4	H
M5	M

FIG. 14

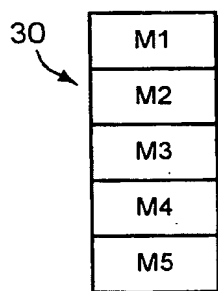


FIG. 15

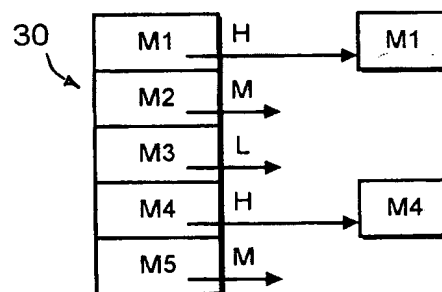


FIG. 16

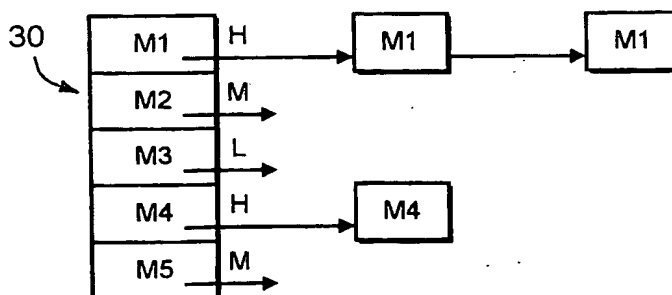
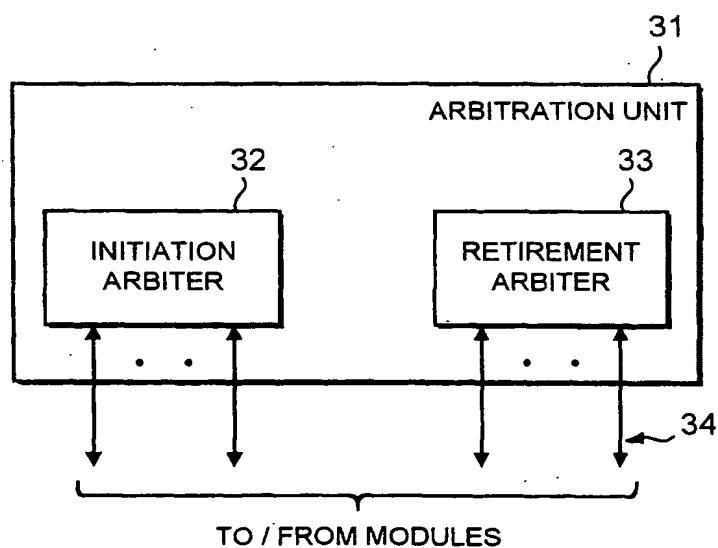
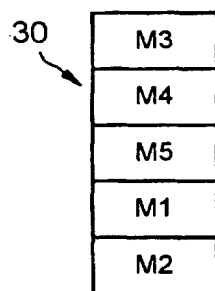
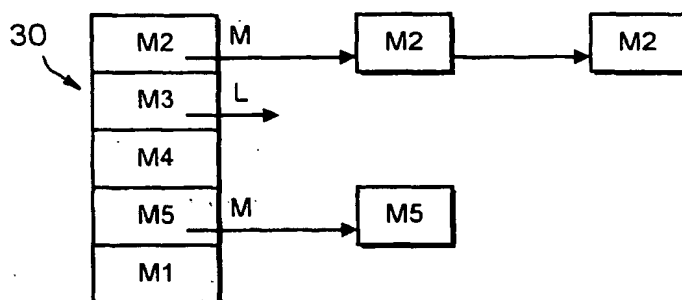


FIG. 17





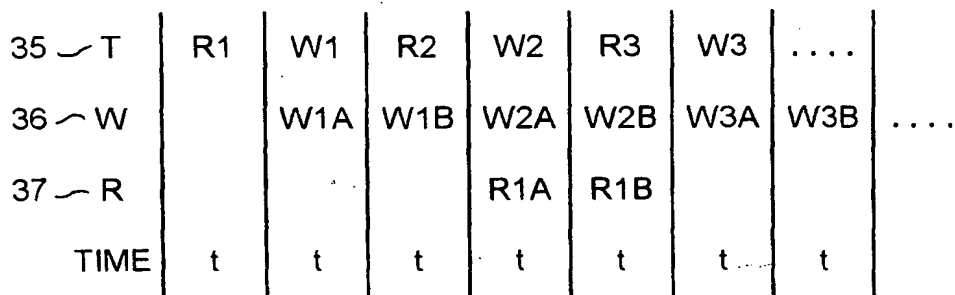


FIG. 21

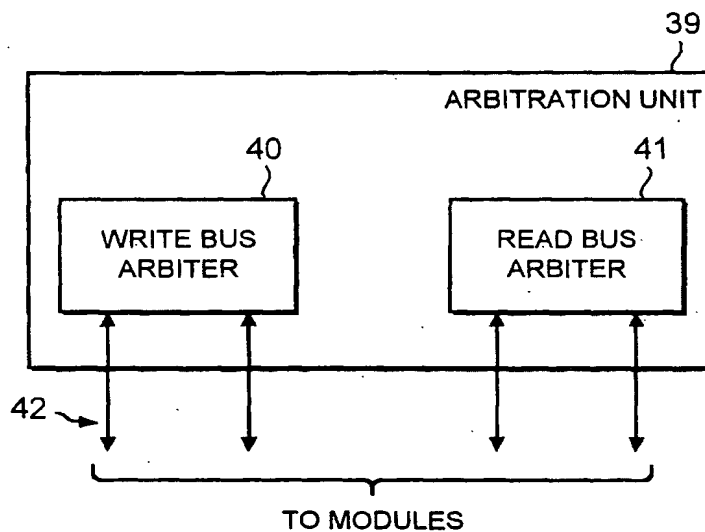


FIG. 22



FIG. 23

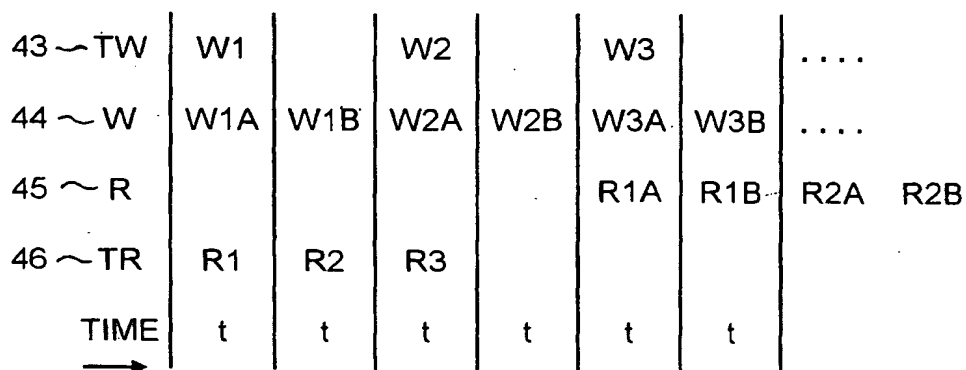


FIG. 24

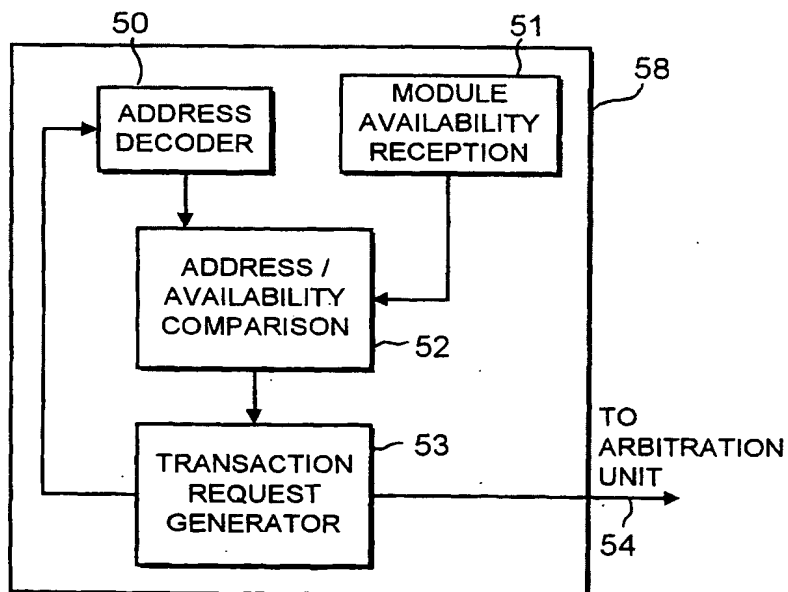


FIG. 25

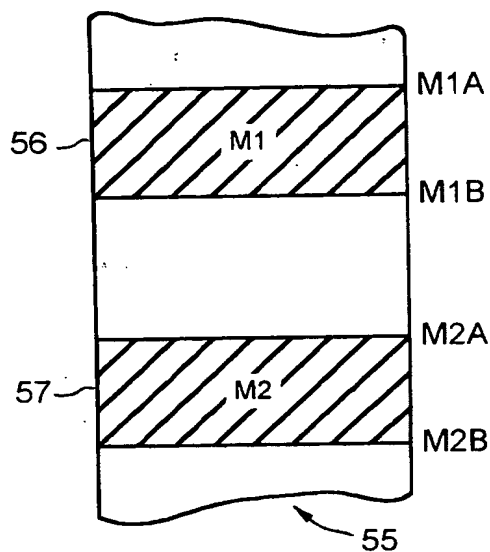


FIG. 26

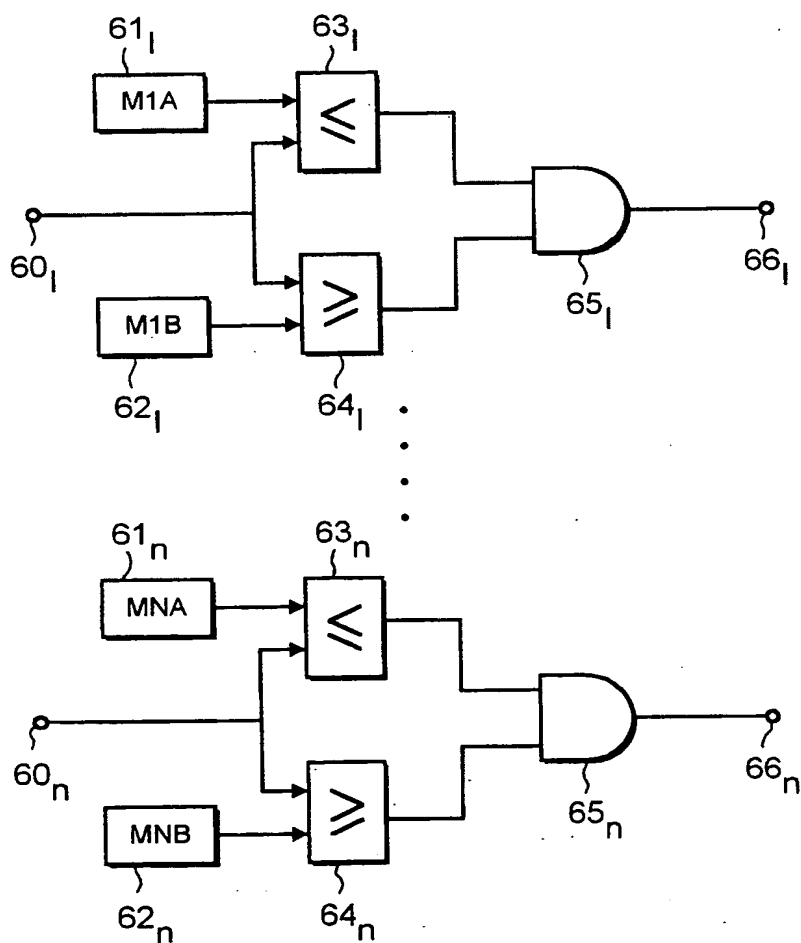


FIG. 27

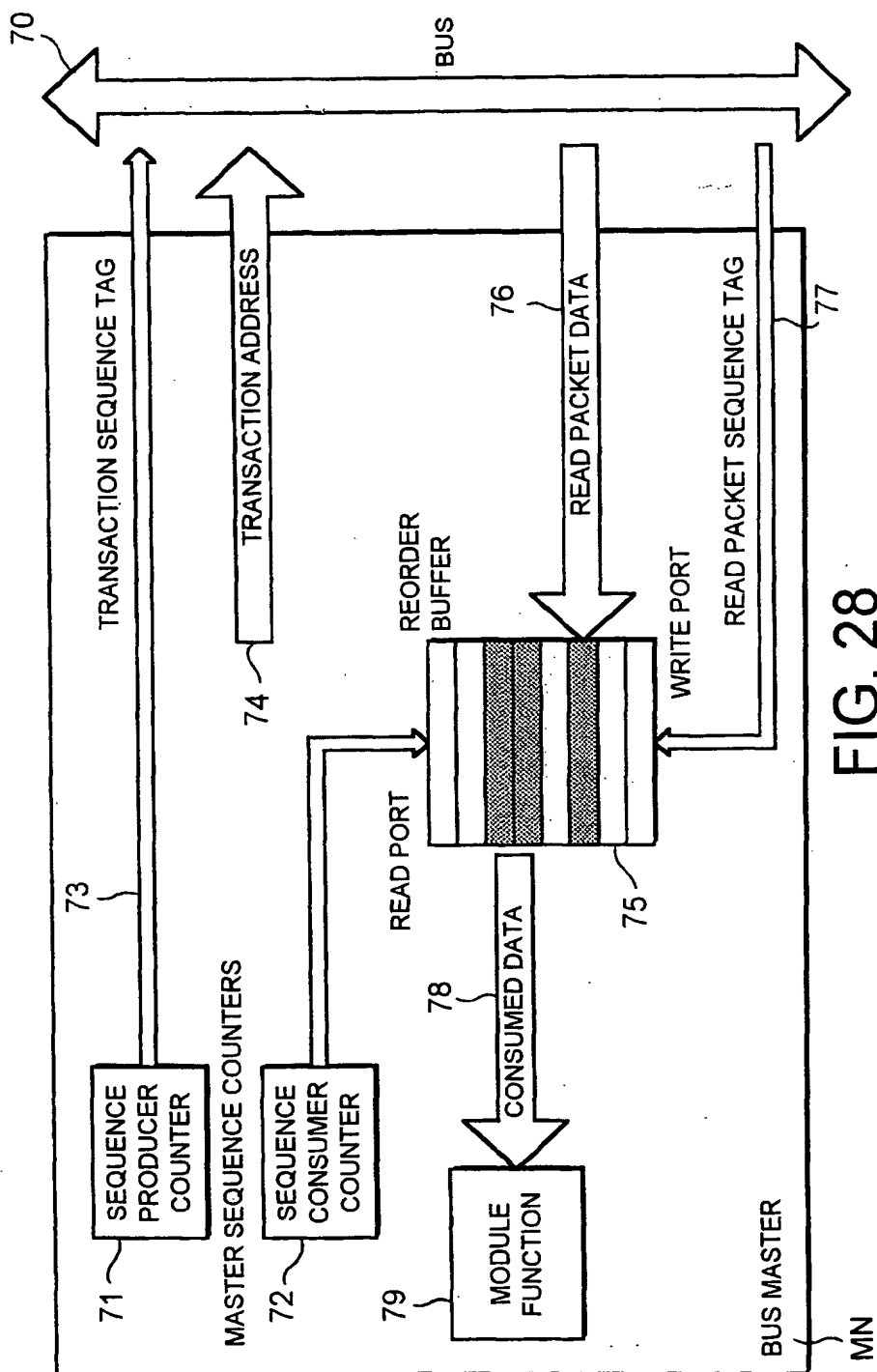


FIG. 28

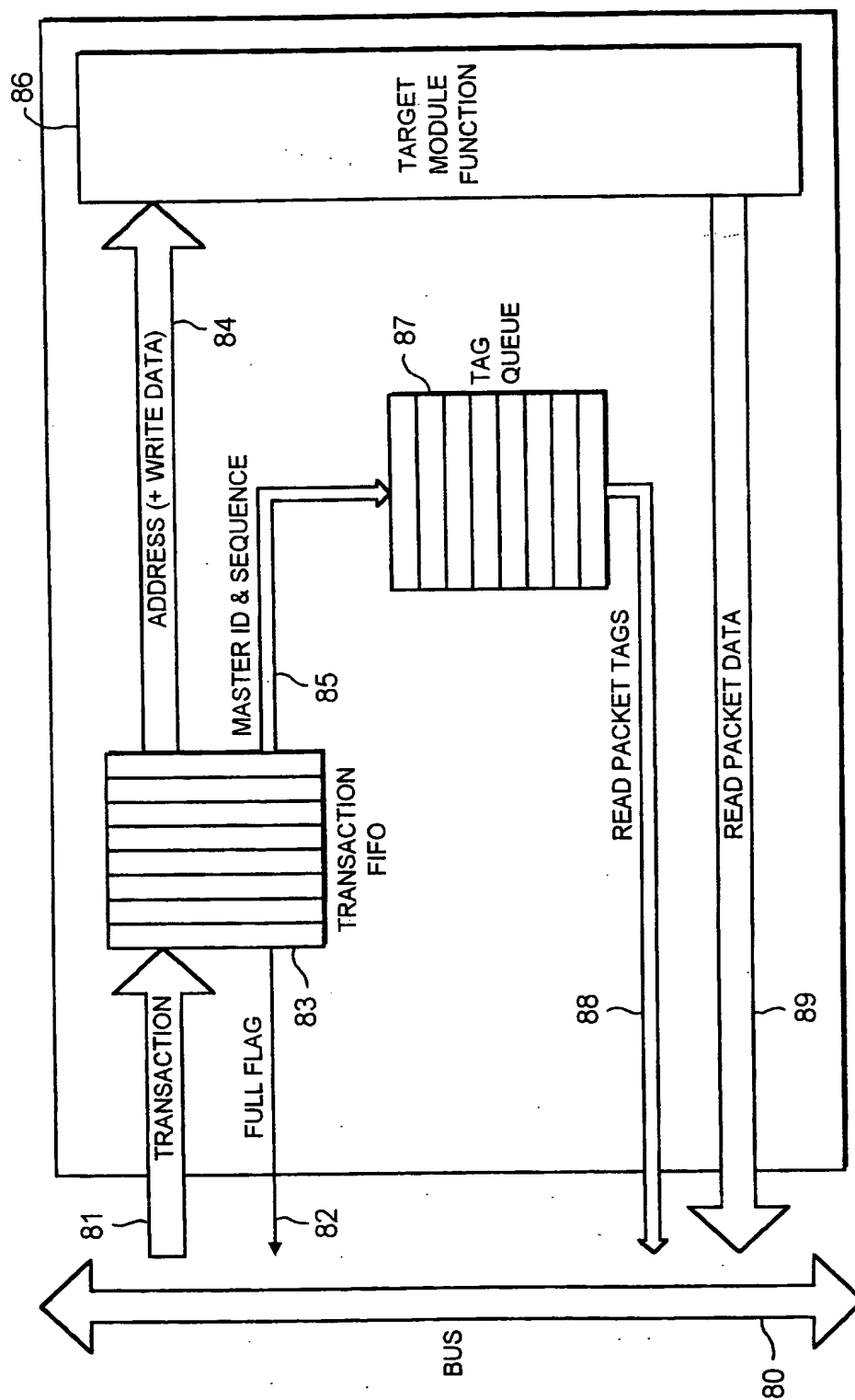


FIG. 29

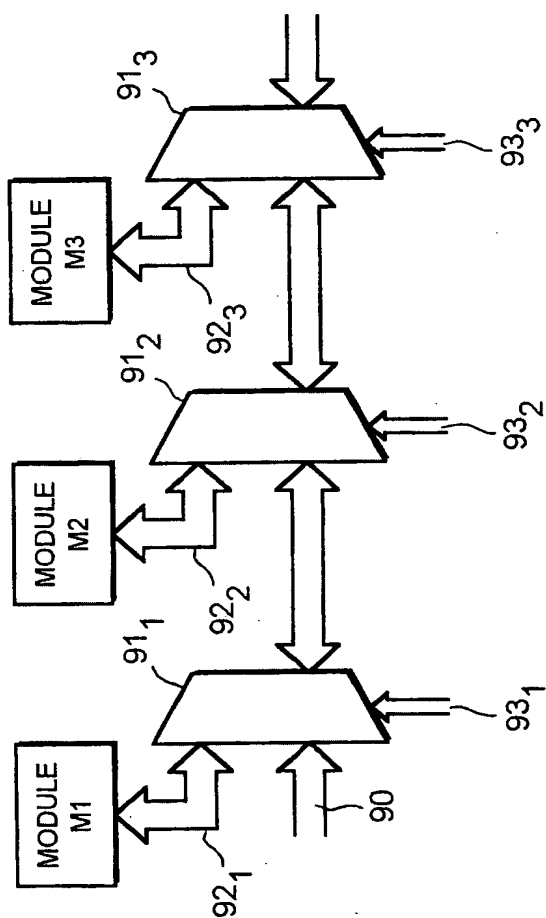


FIG. 30

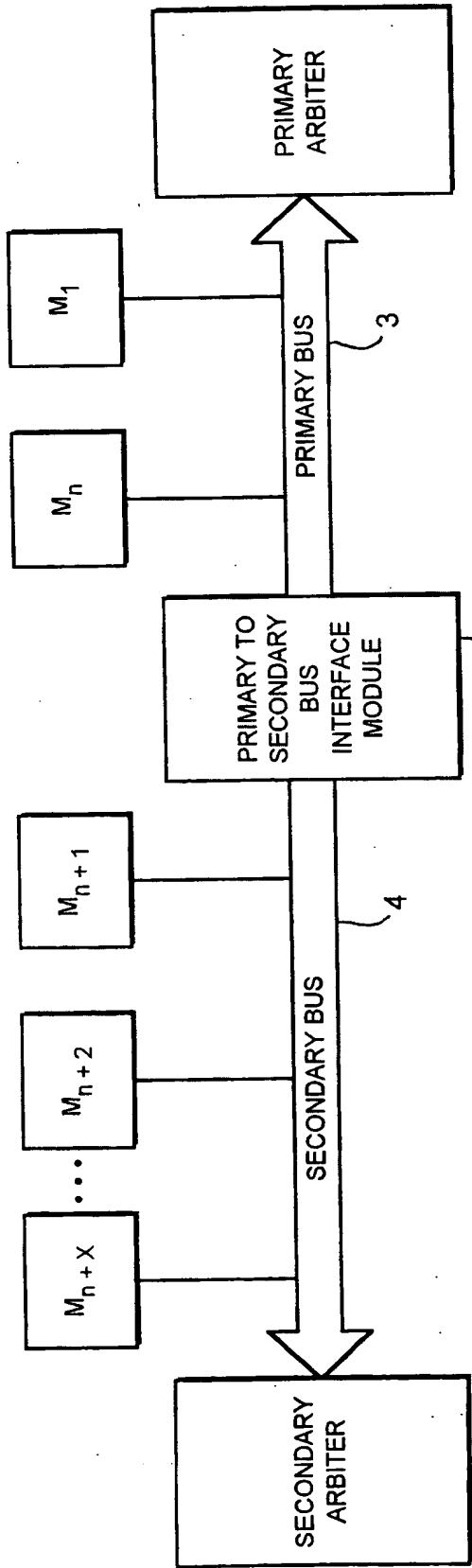


FIG. 31